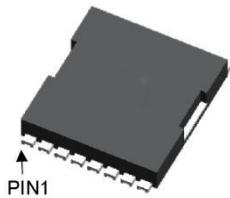


## SGT N-channel Power MOSFET

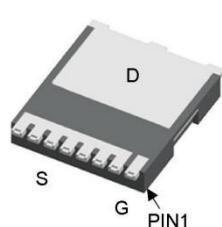
**MTR1R6N08TL**

**TOLL**

TOLL Top View



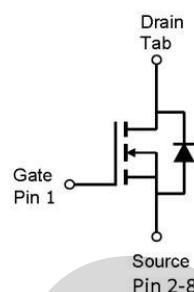
TOLL Bottom View



$V_{DS}$	80	V
$R_{DS(on),TYP}@ V_{GS}=10\text{ V}$	1.27	$\text{m}\Omega$
$I_D$	360	A

### Features

- 1、Low on – resistance
- 2、Package TOLL
- 3、SGT N-channel Power MOSFET



### Applications

- 1、Load Switch for Portable Devices
- 2、DC/DC Converter

**Maximum ratings, at  $T_A = 25^\circ\text{C}$ , unless otherwise specified**

Symbol	Parameter		Rating	Unit
$V(BR)DSS$	Drain-Source breakdown voltage		80	V
$V_{GS}$	Gate-Source voltage		$\pm 20$	V
$I_D$	Continuous drain current @ $V_{GS}=10\text{V}$	$T_C=25^\circ\text{C}$ (Silicon limit)	360	A
		$T_C=100^\circ\text{C}$ (Silicon limit)	227	A
$I_{DM}$	Pulse drain current tested ①	$T_C=25^\circ\text{C}$	1080	A
$E_{AS}$	Avalanche energy, single pulsed ②		2704	$\text{mJ}$
$P_D$	Maximum power dissipation	$T_C=25^\circ\text{C}$	312	W
$T_{STG,TJ}$	Storage and Junction Temperature Range		-55 to +150	$^\circ\text{C}$

## Thermal Characteristics

Symbol	Parameter	Typical	Unit
R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case	0.4	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	62	°C/W

## Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
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### Static Electrical Characteristics @ T<sub>j</sub>=25°C (unless otherwise stated)

V(BR)DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	80	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V	--	--	1	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0	3.0	4.0	V
R <sub>D(on)</sub>	Drain-Source On-State Resistance ④	V <sub>GS</sub> =10V, I <sub>D</sub> =50A	--	1.27	1.6	mΩ
G <sub>fs</sub>	Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =50A	--	227	--	S

### Dynamic Electrical Characteristics @ T<sub>j</sub> = 25°C (unless otherwise stated)

C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V , f=1MHz	--	14140	--	pF
C <sub>oss</sub>	Output Capacitance		--	2259	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	61	--	pF
R <sub>g</sub>	Gate Resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	--	1.85	--	Ω
Q <sub>g</sub> (10V)	Total Gate Charge	V <sub>GS</sub> =10V, V <sub>DS</sub> =40V, I <sub>D</sub> =50A	--	205	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	54	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	46	--	nC

## Switching Characteristics

Td(on)	Turn-on Delay Time	VGS=10V, VDS=40V, RL=3.0Ω, Tj=25°C	--	38	--	ns
Tr	Turn-on Rise Time		--	132	--	ns
Td(off)	Turn-Off Delay Time		--	126	--	ns
Tf	Turn-Off Fall Time		--	153	--	ns

## Source- Drain Diode Characteristics@ T<sub>j</sub> = 25°C (unless otherwise stated)

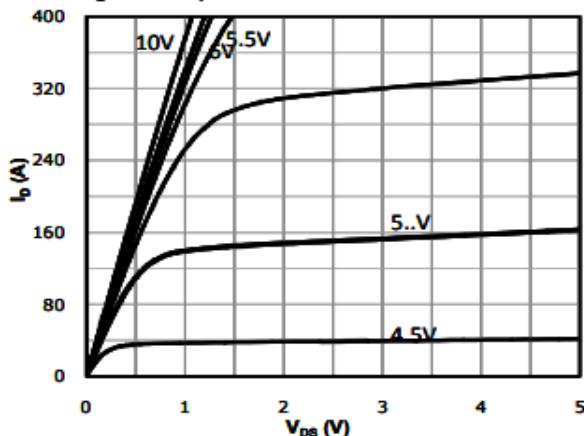
VSD	Forward on voltage	I <sub>SD</sub> =50A, V <sub>GS</sub> =0V	--	0.8	1.2	V
Trr	Reverse Recovery Time	I <sub>F</sub> =30A, di/dt=500A/μs	--	112	--	ns
Qrr	Reverse Recovery Charge	I <sub>F</sub> =30A, di/dt=500A/μs	--	220	--	nC

NOTE: ① Repetitive rating; pulse width limited by max junction temperature.

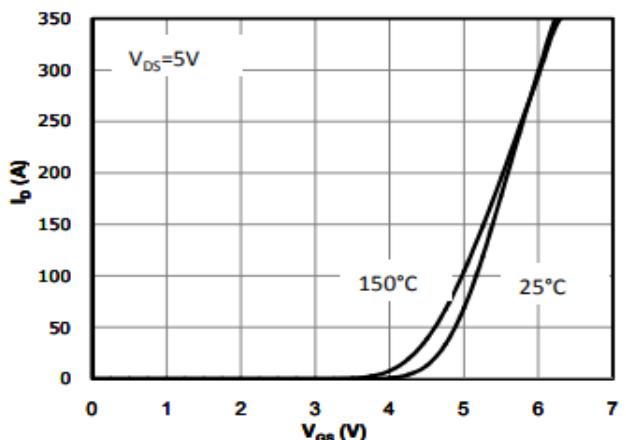
- ② Limited by T<sub>Jmax</sub>, starting T<sub>J</sub> = 25°C, L = 0.5mH, R<sub>G</sub> = 25Ω. Part not recommended for use above this value
- ③ The power dissipation P<sub>DSM</sub> is based on R<sub>θJA</sub> and the maximum allowed junction temperature of 150°C.
- ④ Pulse width ≤ 380μs; duty cycle≤ 2%.

## Typical Performance Characteristics

**Fig 1: Output Characteristics**

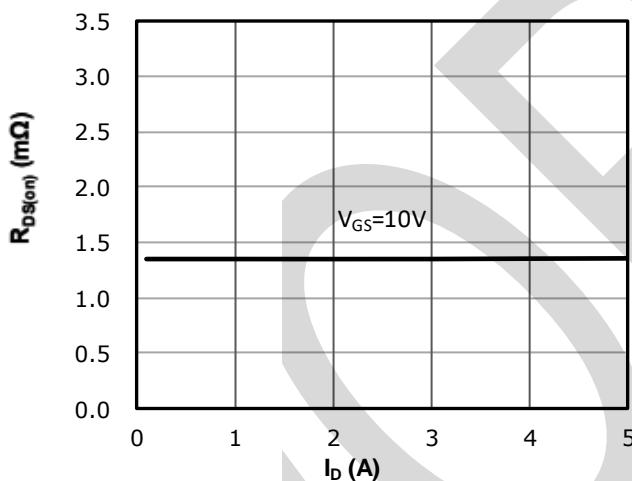


**Fig 2: Transfer Characteristics**

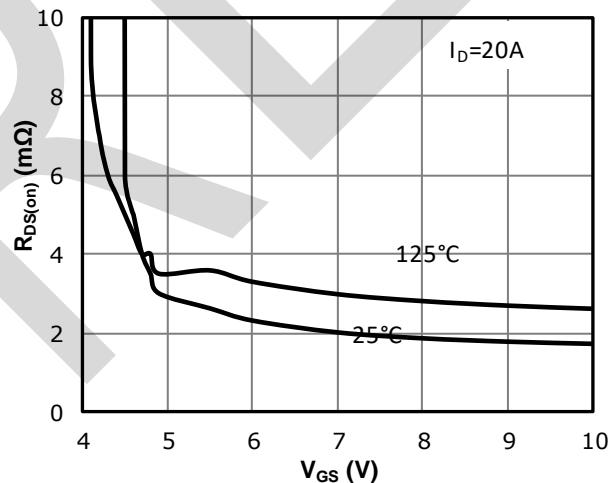


**Fig 3:  $R_{DS(on)}$  Vs  $I_D$**

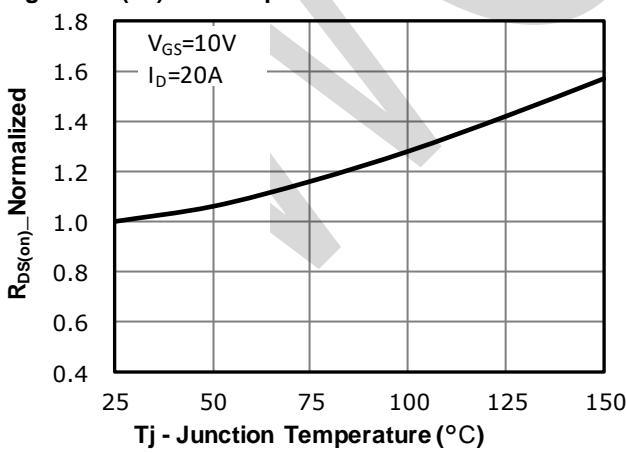
Characteristics( $T_c=25^\circ\text{C}$ )



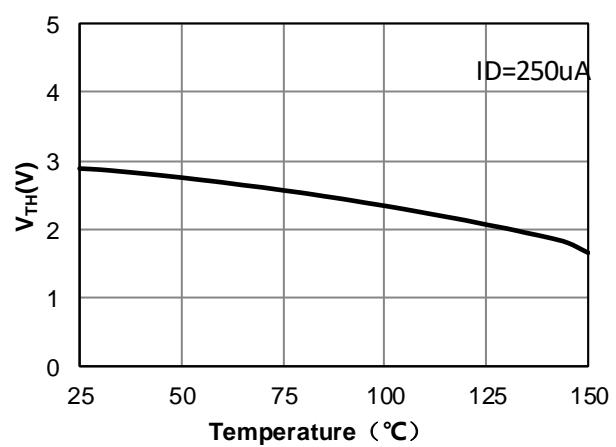
**Fig 4:  $R_{DS(on)}$  vs Gate Voltage**



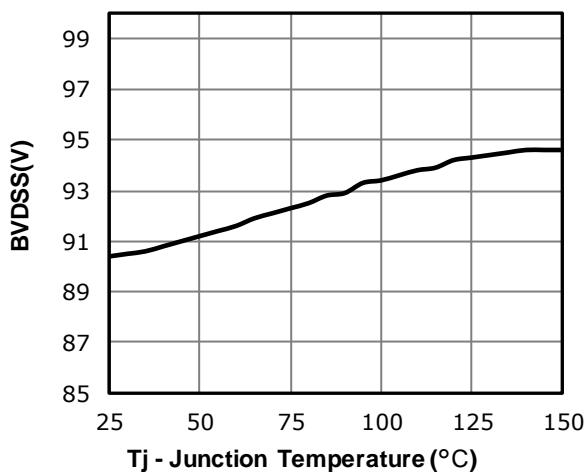
**Fig 5:  $R_{DS(on)}$  vs. Temperature**



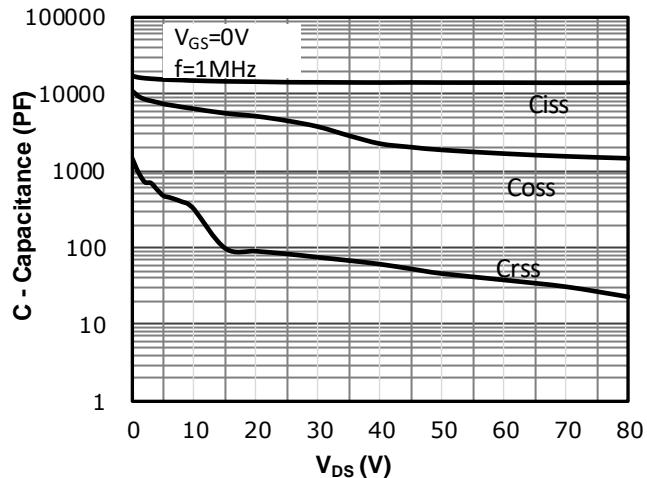
**Fig 6:  $V_{GS(TH)}$  Vs  $T_j$  Temperature Characteristics**



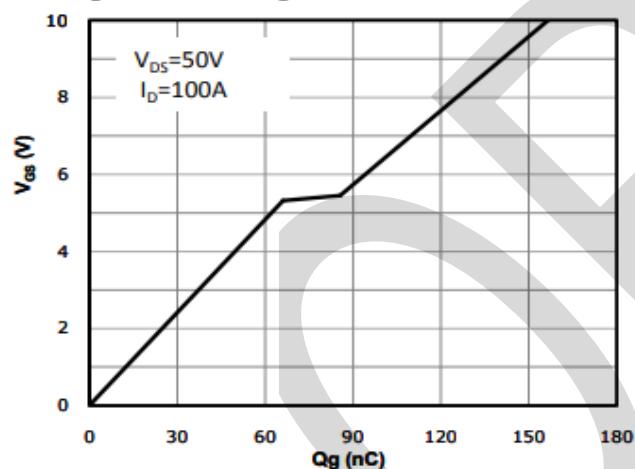
**Fig 7: BVDSS vs. Temperature**



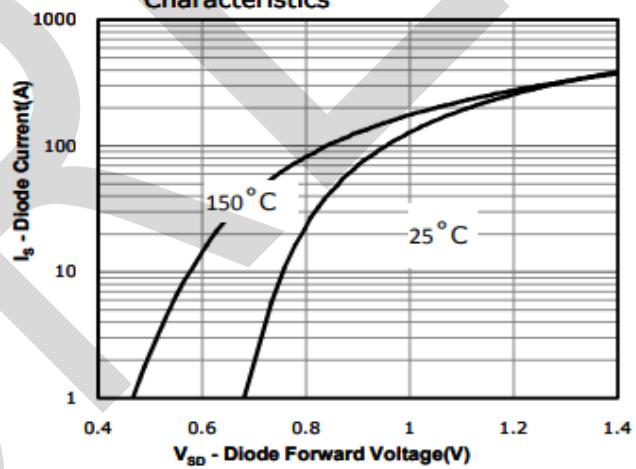
**Fig 8: Capacitance Characteristics**



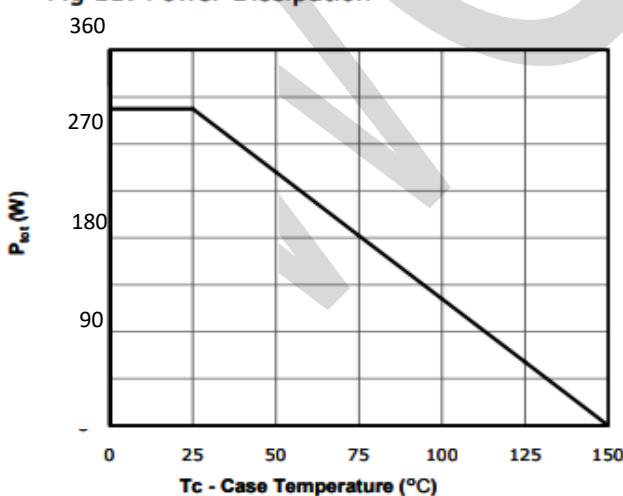
**Fig 9: Gate Charge Characteristics**



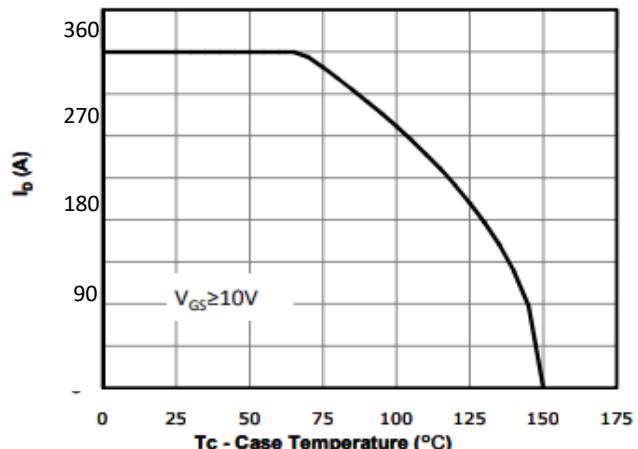
**Fig 10: Body-diode Forward Characteristics**



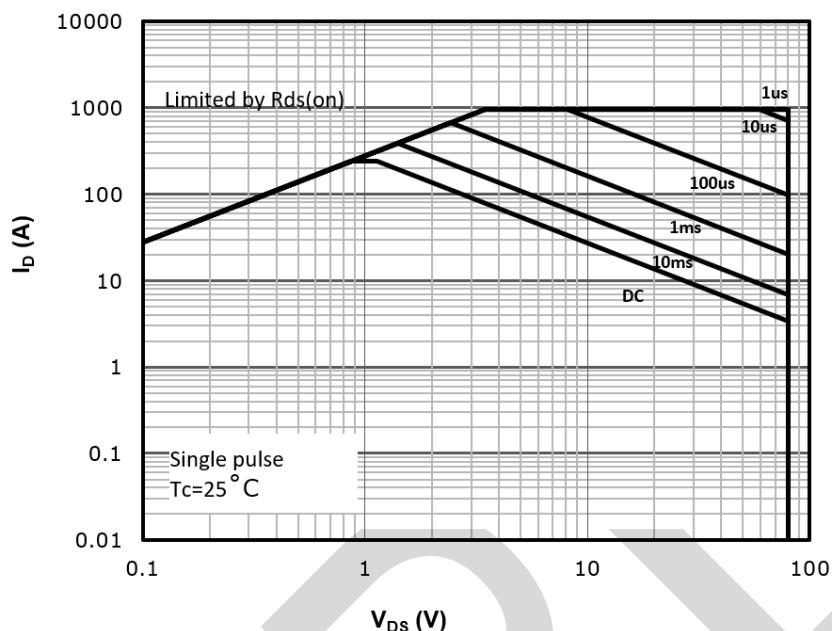
**Fig 11: Power Dissipation**



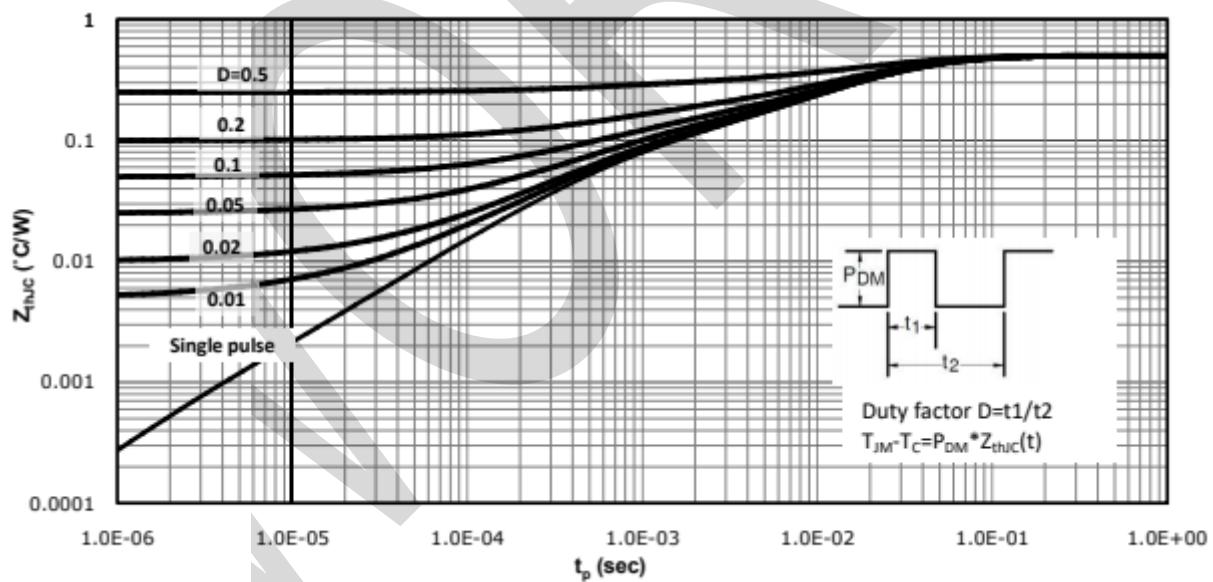
**Fig 12: Drain Current Derating**



**Fig 13: Safe Operating Area**



**Fig 14: Max.Transtient Thermal impedance**



## PACKAGE OUTLINE DIMENSIONS

**TOLL:(MM)**

